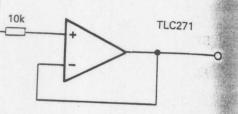
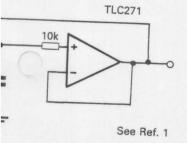
sample and hold.



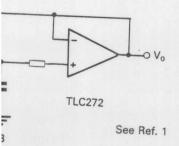
Drift rate $\frac{dV}{dt} = \frac{I_{LEAK}}{C}$

See Ref. 1

sample and hold.

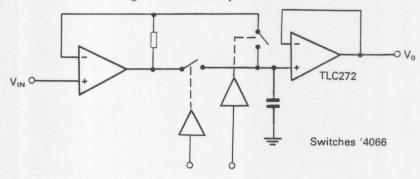


y sample and hold.



DESIGN MANUAL

Fig. 5.6 Fast sample and hold.



RECTIFIERS

Fig. 5.7 Precision full wave rectifier (absolute value circuit).

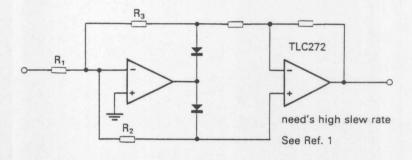


Fig. 5.8 Halfwave rectifier.

